BEST AVAILABLE COPY

DERWENT-ACC-NO:

2002-544493

DERWENT-WEEK:

200258

COPYRIGHT 2005 DERWENT INFORMATION LTD

TITLE:

and a second

Etch apparatus for fabricating semiconductor

INVENTOR: KIM, N J; LEE, H C

PATENT-ASSIGNEE: SAMSUNG ELECTRONICS CO LTD[SMSU]

PRIORITY-DATA: 2000KR-0042693 (July 25, 2000)

PATENT-FAMILY:

PUB-NO

PUB-DATE

LANGUAGE

PAGES MAIN-IPC

KR 2002009190 A

February 1, 2002

N/A

001

H01L 021/306

APPLICATION-DATA:

PUB-NO

APPL-DESCRIPTOR

APPL-NO

APPL-DATE

KR2002009190A

N/A

2000KR-0042693

July 25, 2000

INT-CL (IPC): H01L021/306

ABSTRACTED-PUB-NO: KR2002009190A

BASIC-ABSTRACT:

NOVELTY - An etch apparatus for fabricating a semiconductor is provided to compensate easily a temperature of a reaction chamber by using an end point detector window connected with a cable of a detector.

DETAILED DESCRIPTION - A heating portion(30) is installed in an end point detector window(12). The end point detector window(12) is connected with a cable(18) of a detector. The heating portion(30) is used for compensating a temperature in an etch process of a reaction chamber(10). The heating portion(30) has a heating line. The heating line of the heating portion(30) is wound between the end point detector window(12) and a housing(25) for forming a peripheral region of the end point detector window(12). A power supply portion is used for supplying power to the heating line. A control portion is connected with the power supply portion. The control portion is used for controlling the power supply portion and monitoring an internal temperature of the reaction chamber(10).

